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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/023,117	12/17/2001	Bernardo De Oliveira Kastrup Pereira	NL 000721	2411

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PHILIPS ELECTRONICS NORTH AMERICA CORPORATION  
INTELLECTUAL PROPERTY & STANDARDS  
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SAN JOSE, CA 95131

EXAMINER
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ELLIS, RICHARD L

ART UNIT	PAPER NUMBER
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2183

MAIL DATE	DELIVERY MODE
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06/07/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/023,117	<b>Applicant(s)</b> DE OLIVEIRA KASTRUP PEREIRA ET AL.	
	<b>Examiner</b> Richard Ellis	<b>Art Unit</b> 2183	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 03 April 2007.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

1. Claims 1-20 remain for examination.
2. The text of those sections of Title 35, US Code not included in this action can be found in a prior Office Action.
3. Claims 1-20 are rejected under 35 USC 102(b) as being clearly anticipated by Abbott, U.S. Patent 6,006,321.

Abbott was cited as a prior art reference in paper number 20061207, mailed December 12, 2006.

4. The rejections are respectfully maintained and incorporated by reference as set forth in the last office action, paper number 20061207, mailed December 12, 2006.
5. Applicant's arguments filed April 3, 2007 have been fully considered but they are not deemed to be persuasive.
6. In the remarks, applicant argues in substance:

- A. That: "Abbott does not teach a programmable connection circuit that receives a set of logic output bits and selectively routes the logic output bits to provide the plurality of output bits that are output to a destination register, based on the output ordering instruction, as claimed in claim 1, upon which claims 2-4 depend."

"Abbott's circuit 114 does not receive logic output bits from the configurable logic blocks, and does not route these bits to provide the plurality of output bits that are provided to the destination register by a unit output, as specifically claimed in claim 1."

"Abbot does not teach programming a connection circuit subsequent to the logic blocks so as to perform a routing of outputs of the programmable logic blocks ... as claimed in claim 5. As noted above, Abbott teaches programming a connection circuit to transpose bits between a first logic block and another logic block within the programmable logic block."

"As noted above, Abbott does not teach selectively coupling bits of an output word to bits of a result word, and outputting the result word to a destination identified in a program instruction, as claimed in claim 6"

"Abbott does not teach a connection circuit that receives an output word from a programmable logic block in a first output bit order and provides therefrom a word of bits in a second output bit order that is stored in a destination register of a plurality of registers that also provide the input words, as claimed in claim 7"

"Abbott does not teach a processor that includes a connectino circuit that receives the result word from a confirugable function circuit and provides an output word for storing in the destination register that includes a plurality of bits that are arranged in a bit order based on the programmed set of configuration data, as claimed in claim 13"

This is not found persuasive because applicant is reading the claim language in an overly narrow manner, one that is not required and unstated within the claim language itself. Taking applicant's amended claim 1 as exemplarily, the claim language simply requires a plurality of configurable logic blocks that produce an output, and a programmable connection circuit that receives the outputs from the logic blocks and that itself provides a rearranged output. As seen from Abbott in fig. 4, the element identified as the claimed "second programmable connection circuit", element 410, receives output from blocks 402, 404, 406, and 408, which are themselves a "plurality of independent configurable logic blocks" as claimed. The output from this plurality (402, 404, 406, and 408) forms the input to block 410. Accordingly, blocks 402, 404, 406, and 408 are "produc[ing] a set of logic output bits corresponding to the configured logic function being applied to the set of logic input bits" as claimed. Block 410 is therefore "connected to receive the set of logic output bits" as claimed, and as detailed in the prior action, to "selectively route the logic output bits to provide the plurality of output bits" as claimed. The output from block 410 is processed through blocks 412 and 414, passed out of fig. 4 to block 214 of fig. 2, flows out of box 214 and forms the output of blocks 114,116 of fig. 1, and is then stored in register bank 104 or 106. Accordingly, block 410 is "providing the plurality of output bits" that ultimately are stored in the register file. Accordingly, as amended, block 410 is performing exactly applicant's claimed function. It receives input bits that are the output of configurable functional blocks, and it provides output that is to be stored in the register file, and it performs selective routing to the extent that the selective routing has been claimed.

Claimed subject matter, not the specification, is the measure of invention. Limitations in the specification cannot be read into the claims for the purpose of avoiding the prior art. *In re Self*, 213 USPQ 1,5 (CCPA 1982); *In re Priest*, 199 USPQ 11,15 (CCPA 1978).

"It is the claims that measure the invention." *SRI Int'l v. Matshshita Elec. Corp.*, 775 F.2d 1107, 1121, 227 USPQ 577, 585 (Fed. Cir. 1985) (en banc).

"The invention disclosed in Hiniker's written description may be outstanding in its field, but the name of the game is the claim." *In re Hiniker Co.*, 47 USPQ2d 1523, 1529 (Fed. Cir. 1998).

"[A]s an initial matter, the PTO applies to the verbiage of the proposed claims the broadest reasonable meaning of the words in their ordinary usage as they would be understood by one of ordinary skill in the art, taking into account whatever enlightenment by way of definitions or otherwise that may be afforded by the written description contained in the applicant's specification." *In re Morris*, 44 USPQ2d 1023, 1027 (Fed. Cir. 1997).

"limitations appearing in the specification will not be read into the claims, and ... interpreting what is meant by a word in a claim 'is not to be confused with adding an extraneous limitation appearing in the specification, which is improper'." *Intervet Am., v. Kee-Vet Labs.*, 12 USPQ2d 1474, 1476 (Fed. Cir. 1989)(citation omitted).

"it is entirely proper to use the specification to interpret what the patentee meant by a word or phrase in the claim, ... this is not to be confused with adding an extraneous limitation appearing in the specification, which is improper. By 'extraneous,' we mean a limitation read into a claim from the specification wholly apart from any need to interpret ... particular words or phrases in the claim." *In re Paulsen*, 31 USPQ2d 1671, 1674 (Fed. Cir. 1994) (citation omitted).

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR § 1.136(a). The practice of automatically extending the shortened statutory period an additional month upon the filing of a timely first response to a final rejection has been discontinued by the Office. See 1021 TMOG 35.

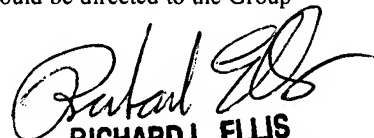
A SHORTENED STATUTORY PERIOD FOR RESPONSE TO THIS FINAL ACTION IS SET TO EXPIRE THREE MONTHS FROM THE DATE OF THIS ACTION. IN THE EVENT A FIRST RESPONSE IS FILED WITHIN TWO MONTHS OF THE MAILING DATE OF THIS FINAL ACTION AND THE ADVISORY ACTION IS NOT MAILED UNTIL AFTER THE END OF THE THREE-MONTH SHORTENED STATUTORY PERIOD, THEN THE SHORTENED STATUTORY PERIOD WILL EXPIRE ON THE DATE THE ADVISORY ACTION IS MAILED, AND ANY EXTENSION FEE PURSUANT TO 37 CFR § 1.136(a) WILL BE CALCULATED FROM THE MAILING DATE OF THE ADVISORY ACTION. IN NO EVENT WILL THE STATUTORY PERIOD FOR RESPONSE EXPIRE LATER THAN SIX MONTHS FROM THE DATE OF THIS FINAL ACTION.

8. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Richard Ellis whose telephone number is (571) 272-4165. The Examiner can normally be reached on Monday through Thursday from 7am to 5pm.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Eddie Chan, can be reached on (571) 272-4162. The fax phone number for the USPTO is: (703)872-9306.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (571) 272-2100.

Richard Ellis  
May 29, 2007

  
**RICHARD L. ELLIS**  
**PRIMARY EXAMINER**